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PATENT
Attorney Docket No.: SAM-0250

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Sang-Young Kim, *et al.* Examiner: Not yet assigned
Serial No.: 10/038,299 Group Art Unit: 2817
Filing Date: January 4, 2002
Title: PHASE LOCKED LOOP CIRCUIT FOR A FRACTIONAL-N FREQUENCY
SYNTHESIZER

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8

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8-6-02

Date

Vanessa Marakas

Vanessa Marakas

Assistant Commissioner for Patents
Office of Initial Patent Examination
Customer Service Center
Washington, D.C. 20231

REQUEST FOR CORRECTION TO FILING RECEIPT

Sir:

The Official Filing Receipt received in connection with the above-identified patent application is believed to be in error. The listed order of the Applicants is incorrect. Inventor "Sang-Young Kim" should be listed first and Inventor "Pil-Jae Jun" should be listed second. A copy of the incorrect Filing Receipt with the requested change noted thereon is enclosed.

Respectfully submitted,

Date: August 6, 2002
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/038,299	01/04/2002	2817	740	SAM-0250	9	16	1

CONFIRMATION NO. 8434

 Steven M. Mills
 MILLS & ONELLO LLP
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 Boston, MA 02108

FILING RECEIPT



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Date Mailed: 07/26/2002

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 Applicant(s) *Sang-Young Kim, Euijungboo-shi, Korea, Republic of;*
 Pil-Jae Jun, Suwon, KOREA, REPUBLIC OF;
~~Sang-Young Kim, Euijungboo-shi, KOREA, REPUBLIC OF;~~

Assignment For Published Patent Application

Samsung Electronics Co., Ltd.;

Domestic Priority data as claimed by applicant

Foreign Applications

REPUBLIC OF KOREA 2001-15161 03/23/2001

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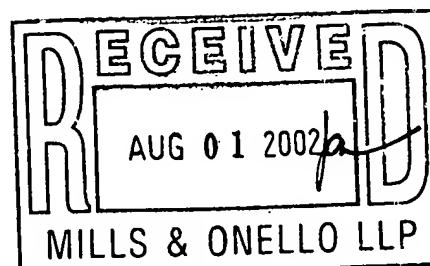
Projected Publication Date: 09/26/2002

Non-Publication Request: No

Early Publication Request: No

Title

Phase locked loop circuit for a fractional-N frequency synthesizer

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Preliminary Class

331

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